



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

\*BIBDATASHEET\*

Bib Data Sheet

CONFIRMATION NO. 2217

|                             |                                       |              |                        |                                     |
|-----------------------------|---------------------------------------|--------------|------------------------|-------------------------------------|
| SERIAL NUMBER<br>10/693,075 | FILING DATE<br>10/23/2003<br><br>RULE | CLASS<br>716 | GROUP ART UNIT<br>2825 | ATTORNEY DOCKET NO.<br>031344/2912P |
|-----------------------------|---------------------------------------|--------------|------------------------|-------------------------------------|

APPLICANTS

Lalita Satapathy, Milpitas, CA;  
 Santhanakris Raman, Fremont, CA;  
 Richard Blinne, Fort Collins, CO;

\*\* CONTINUING DATA *NONE HR*

\*\* FOREIGN APPLICATIONS *NONE HR*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 01/22/2004

|  |   |                        |                        |                       |                            |
|--|---|------------------------|------------------------|-----------------------|----------------------------|
| Foreign Priority claimed<br>35 USC 119 (a-d) conditions met<br>Verified and Acknowledged | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no<br><input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance<br><i>Helen Koneshuk</i> <i>HR</i><br>Examiner's Signature Initials | STATE OR COUNTRY<br>CA | SHEETS<br>DRAWING<br>7 | TOTAL<br>CLAIMS<br>26 | INDEPENDENT<br>CLAIMS<br>2 |
|--|---|------------------------|------------------------|-----------------------|----------------------------|

ADDRESS  
 24319  
 LSI LOGIC CORPORATION  
 1621 BARBER LANE  
 MS: D-106  
 MILPITAS, CA  
 95035

TITLE  
 Comparison of two hierarchical netlist to generate change orders for updating an integrated circuit layout

|                                   |   |   |
|-----------------------------------|---|---|
| FILING FEE<br><br>RECEIVED<br>878 | FEES: Authority has been given in Paper<br>No. _____ to charge/credit DEPOSIT ACCOUNT<br>No. _____ for following: | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )<br><input type="checkbox"/> 1.18 Fees ( Issue )<br><input type="checkbox"/> Other _____<br><input type="checkbox"/> Credit |
|-----------------------------------|---|---|